IEEE HOME I SEARCH IEEE I SHOP I WEB ACCOUNT I CONTACT IEEE

Membership   Publications/Services   Standards   Conferences   Careers/Jobs

IEEE Xplore®
RELEASE 1.4

Welcome
United States Patent and Trademark Of

Help   FAQ   Terms   IEEE Peer   Quick Links   » Sea
Review

Welcome to IEEE Xplore

- Home
- What Can
  I Access?
- Log-out

Tables of Contents

- Journals
  & Magazines
- Conference
  Proceedings
- Standards

Search

- By Author
- Basic
- Advanced

Member Services

- Join IEEE
- Establish IEEE
  Web Account
- Access the
  IEEE Member
  Digital Library

🖶 Print Format

Your search matched **43** of **928462** documents.

A maximum of **43** results are displayed, **25** to a page, sorted by **Relevance** in **descending** order.
You may refine your search by editing the current search expression or entering a new one the text b
Then click **Search Again**.

((emulation and fpga and prototype) and ((1950 <in> py) or (1951 <in> py) or (1952 <ir

Search Again

Results:
Journal or Magazine = **JNL**   Conference = **CNF**   Standard = **STD**

---

1 **Using FPGAs to implement reconfigurable DSP architectures?**
Monaghan, S.; Cowen, C.; Noakes, P.D.;
Field Programmable Gate Arrays - Technology and Applications, IEE Colloquium
15 Feb 1993
Page(s): 7/1 -7/3

[Abstract]   [PDF Full-Text (176 KB)] **IEE CNF**

---

2 **System development using Actel field programmable gate arrays**
McCarty, D.;
Compcon Spring '90. 'Intellectual Leverage'. Digest of Papers. Thirty-Fifth IEEE
Computer Society International Conference. , 26 Feb-2 Mar 1990
Page(s): 470 -476

[Abstract]   [PDF Full-Text (392 KB)] **IEEE CNF**

---

3 **Hardware emulation board based on FPGAs and programmable
interconnections**
Lo, W.Y.; Choy, C.S.; Chan, C.F.;
Rapid System Prototyping, 1994. 'Shortening the Path from Specification to Pro
Proceedings., Fifth International Workshop on , 21-23 Jun 1994
Page(s): 126 -130

[Abstract]   [PDF Full-Text (384 KB)] **IEEE CNF**

---

4 **Buffer memory requirements in DSP applications**
Ade, M.; Lauwereins, R.; Peperstraete, J.A.;

Rapid System Prototyping, 1994. 'Shortening the Path from Specification to Pro
Proceedings., Fifth International Workshop on , 21-23 Jun 1994
Page(s): 108 -123

[Abstract]   [PDF Full-Text (896 KB)] **IEEE CNF**

5 **Project Spinnaker: a new generation of rapid prototyping system**
Courtoy, M.;
Rapid System Prototyping, 1994. 'Shortening the Path from Specification to Pro
Proceedings., Fifth International Workshop on , 21-23 Jun 1994
Page(s): 141 -144

[Abstract]   [PDF Full-Text (324 KB)] **IEEE CNF**

6 **Using an FPGA based computer as a hardware emulator for built-in se
structures**
Wieler, R.W.; Zhang, Z.; McLeod, R.D.;
Rapid System Prototyping, 1994. 'Shortening the Path from Specification to Pro
Proceedings., Fifth International Workshop on , 21-23 Jun 1994
Page(s): 16 -21

[Abstract]   [PDF Full-Text (396 KB)] **IEEE CNF**

7 **Emulation of the Sparcle microprocessor with the MIT Virtual Wires
emulation system**
Dahl, M.; Babb, J.; Tessier, R.; Hanono, S.; Hoki, D.; Agarwal, A.;
FPGAs for Custom Computing Machines, 1994. Proceedings. IEEE Workshop on
Apr 1994
Page(s): 14 -22

[Abstract]   [PDF Full-Text (672 KB)] **IEEE CNF**

8 **A prototyping system for verification and evaluation in hardware-soft
cosynthesis**
Benner, T.; Ernst, R.; Konenkamp, I.; Schuler, P.; Schaub, H.-C.;
Rapid System Prototyping, 1995. Proceedings., Sixth IEEE International Worksh
7-9 Jun 1995
Page(s): 54 -59

[Abstract]   [PDF Full-Text (452 KB)] **IEEE CNF**

9 **Hardware-software codesign with GRAPE**
Ade, M.; Lauwereins, R.; Peperstraete, J.A.;
Rapid System Prototyping, 1995. Proceedings., Sixth IEEE International Worksh

7-9 Jun 1995
Page(s): 40 -47

[Abstract]   [PDF Full-Text (664 KB)] **IEEE CNF**

10 **Circuit partitioning with partial order for mixed simulation emulation environment**
Manku, G.S.; Kumar, A.; Kumar, S.;
Rapid System Prototyping, 1995. Proceedings., Sixth IEEE International Worksh
7-9 Jun 1995
Page(s): 201 -207

[Abstract]   [PDF Full-Text (424 KB)] **IEEE CNF**

11 **Modeling a versatile FPGA for prototyping adaptive systems**
Kwiat, K.A.; Debany, W.H., Jr.; Hariri, S.;
Rapid System Prototyping, 1995. Proceedings., Sixth IEEE International Worksh
7-9 Jun 1995
Page(s): 174 -180

[Abstract]   [PDF Full-Text (496 KB)] **IEEE CNF**

12 **Rapid prototyping of DSP systems: requirements and solutions**
Note, S.; van Lierop, P.; van Ginderdeuren, J.;
Rapid System Prototyping, 1995. Proceedings., Sixth IEEE International Worksh
7-9 Jun 1995
Page(s): 88 -96

[Abstract]   [PDF Full-Text (496 KB)] **IEEE CNF**

13 **Emulation: prototyping without the hassles of FPGA-to-ASIC convers**
Courtoy, M.;
WESCON/'95. Conference record. 'Microelectronics Communications Technology
Producing Quality Products Mobile and Portable Power Emerging Technologies'
Nov 1995
Page(s): 283

[Abstract]   [PDF Full-Text (232 KB)] **IEEE CNF**

14 **Routability improvement using dynamic interconnect architecture**
Li, J.; Cheng, C.-K.;
FPGAs for Custom Computing Machines, 1995. Proceedings. IEEE Symposium o
19-21 Apr 1995
Page(s): 61 -67

[Abstract]   [PDF Full-Text (520 KB)] **IEEE CNF**

15 **A row-based FPGA for single and multiple stuck-at fault detection**
Chen, X.T.; Huang, W.K.; Lombardi, F.; Sun, X.;
Defect and Fault Tolerance in VLSI Systems, 1995. Proceedings., 1995 IEEE
International Workshop on, , 13-15 Nov 1995
Page(s): 225 -233

[Abstract]   [PDF Full-Text (388 KB)] **IEEE CNF**

16 **Real-time emulation of DSP applications on programmable DSPs and**

Lauwereins, R.; Ade, M.; Note, S.;
Electronics, Circuits, and Systems, 1996. ICECS '96., Proceedings of the Third I
International Conference on , Volume: 1 , 13-16 Oct 1996
Page(s): 554 -557 vol.1

[Abstract]   [PDF Full-Text (348 KB)] **IEEE CNF**

17 **Rapid prototyping for communications design validation**
Courtoy, M.;
Northcon/96 , 4-6 Nov 1996
Page(s): 162 -167

[Abstract]   [PDF Full-Text (740 KB)] **IEEE CNF**

18 **Rapid prototyping for communications design validation**
Courtoy, M.;
Southcon/96. Conference Record , 25-27 Jun 1996
Page(s): 49 -54

[Abstract]   [PDF Full-Text (664 KB)] **IEEE CNF**

19 **Implementing DSP applications on heterogeneous targets using min size data buffers**
Ade, M.; Lauwereins, R.; Peperstrate, J.A.;
Rapid System Prototyping, 1996. Proceedings., Seventh IEEE International Wor
on , 19-21 Jun 1996
Page(s): 166 -172

[Abstract]   [PDF Full-Text (552 KB)] **IEEE CNF**

20 **Rapid-prototyping of a CAN-Bus controller: a case study**
Kirschbaum, A.; Renner, F.M.; Wilmes, A.; Glesner, M.;
Rapid System Prototyping, 1996. Proceedings., Seventh IEEE International Wor
on , 19-21 Jun 1996
Page(s): 146 -151

[Abstract]   [PDF Full-Text (560 KB)] **IEEE CNF**

21 **Real-time emulation method for ATM switching systems in broadban**

Matsumura, T.; Yamanaka, N.; Yamaguchi, R.; Ishikawa, K.;
Rapid System Prototyping, 1996. Proceedings., Seventh IEEE International Wor
on , 19-21 Jun 1996
Page(s): 55 -60

[Abstract]   [PDF Full-Text (488 KB)] **IEEE CNF**

22 **Hierarchical partitioning in a rapid prototyping environment**
Ober, U.; Herpel, H.-J.; Glesner, M.;
Rapid System Prototyping, 1996. Proceedings., Seventh IEEE International Wor
on , 19-21 Jun 1996
Page(s): 30 -35

[Abstract]   [PDF Full-Text (544 KB)] **IEEE CNF**

23 **A novel approach to real-time verification of transport system design**
FPGA based emulator
Hayashi, K.; Miyazaki, T.; Shirakawa, K.; Yamada, K.; Ichimori, T.; Fukami, K.
N.;
Rapid System Prototyping, 1996. Proceedings., Seventh IEEE International Wor
on , 19-21 Jun 1996
Page(s): 5 -10

[Abstract]   [PDF Full-Text (640 KB)] **IEEE CNF**

24 **A smoothly upgradable approach to virtual emulation of HW/SW sys**
Borgatti, M.; Rambaldi, R.; Gori, G.; Guerrieri, R.;
Rapid System Prototyping, 1996. Proceedings., Seventh IEEE International Wor
on , 19-21 Jun 1996
Page(s): 83 -88

[Abstract]   [PDF Full-Text (588 KB)] **IEEE CNF**

25 **Using synthesis, simulation, and hardware emulation to prototype a**

pipelined RISC computer system
Hamblen, J.O.;
Microelectronic Systems Education, 1997. MSE '97. Proceedings., 1997 IEEE
International Conference on , 21-23 Jul 1997
Page(s): 131 -132

[Abstract]   [PDF Full-Text (156 KB)] **IEEE CNF**

1 2 [Next]

Home | Log-out | Journals | Conference Proceedings | Standards | Search by Author | Basic Search | Advanced Search
Join IEEE | Web Account | New this week | OPAC Linking Information | Your Feedback | Technical Support | Email Alerting
No Robots Please | Release Notes | IEEE Online Publications | Help | FAQ| Terms | Back to Top

IEEE HOME I SEARCH IEEE I SHOP I WEB ACCOUNT I CONTACT IEEE

| Membership | Publications/Services | Standards | Conferences | Careers/Jobs |

IEEE *Xplore*®
RELEASE 1.4

Welcome
United States Patent and Trademark Of

Help   FAQ   Terms   IEEE Peer   Quick Links   »Sea
Review

Welcome to IEEE *Xplore*®

- Home
- What Can I Access?
- Log-out

Tables of Contents

- Journals & Magazines
- Conference Proceedings
- Standards

Search

- By Author
- Basic
- Advanced

Member Services

- Join IEEE
- Establish IEEE Web Account
- Access the IEEE Member Digital Library

Print Format

Your search matched **17** of **928462** documents.

A maximum of **17** results are displayed, **25** to a page, sorted by **Relevance** in **descending** order.
You may refine your search by editing the current search expression or entering a new one the text b
Then click **Search Again.**

((eda and prototype) and ((1950 <in> py) or (1951 <in> py) or (1952 <in> py) or (1953

Search Again

Results:
Journal or Magazine = **JNL**   Conference = **CNF**   Standard = **STD**

---

1 **Total digital ASIC control for an induction motor drive**
Patterson, E.B.; Morley, D.; Oswald, C.G.; Holmes, P.G.;
ASIC Technology for Power Electronics Equipment, IEE Colloquium on , 20 Feb
Page(s): 2/1 -2/4

[Abstract]   [PDF Full-Text (200 KB)] **IEE CNF**

---

2 **Exploratory orbit analysis**
Michelotti, L.;
Particle Accelerator Conference, 1989. 'Accelerator Science and Technology'.,
Proceedings of the 1989 IEEE , 20-23 Mar 1989
Page(s): 1274 -1276 vol.2

[Abstract]   [PDF Full-Text (344 KB)] **IEEE CNF**

---

3 **ASIC chip set for a cycloconverter drive PCB**
Patterson, E.B.; Morley, D.; Oswald, C.G.; Holmes, P.G.;
Euro ASIC '92, Proceedings. , 1-5 Jun 1992
Page(s): 332 -335

[Abstract]   [PDF Full-Text (308 KB)] **IEEE CNF**

---

4 **Improving FSMD design assurance**
Auletta, R.;
ASIC Conference and Exhibit, 1993. Proceedings., Sixth Annual IEEE Internatio
Sep-1 Oct 1993
Page(s): 274 -277

[Abstract]   [PDF Full-Text (328 KB)] **IEEE CNF**

---

5 **Setting up a retrieval system for design reuse - experiences and acce**
Buttner, G.H.;
Design Automation Conference, 1995, with EURO-VHDL, Proceedings EURO-DA
European , 18-22 Sep 1995
Page(s): 575 -578

[Abstract]   [PDF Full-Text (280 KB)] **IEEE CNF**

---

6 **Distributed EDA tool integration: the PPP paradigm**
Benini, L.; Bogliolo, A.; De Micheli, G.;
Computer Design: VLSI in Computers and Processors, 1996. ICCD '96. Proceed
1996 IEEE International Conference on , 7-9 Oct 1996
Page(s): 448 -453

[Abstract]   [PDF Full-Text (776 KB)] **IEEE CNF**

---

7 **Statistical methods for stress screen development**
Cooper, M.R.;
Electronic Components and Technology Conference, 1996. Proceedings., 46th ,
May 1996
Page(s): 925 -930

[Abstract]   [PDF Full-Text (452 KB)] **IEEE CNF**

---

8 **A sockets-based implementation of hardware and software co-design**
Herbert, A.;
Verilog HDL Conference, 1996. Proceedings., 1996 IEEE International , 26-28 F
1996
Page(s): 77 -80

[Abstract]   [PDF Full-Text (268 KB)] **IEEE CNF**

---

9 **Integrating system level design and instrumentation for communicat
development**
Palmer, F.;
ARFTG Conference Digest, 1998. Computer-Aided Design and Test for High-Spe
Electronics. 52nd , 1998
Page(s): 140 -156

[Abstract]   [PDF Full-Text (784 KB)] **IEEE CNF**

10 **Geographic visualization: designing manipulable maps for exploring temporally varying georeferenced statistics**
MacEachren, A.M.; Boscoe, F.P.; Haug, D.; Pickle, L.W.;
Information Visualization, 1998. Proceedings. IEEE Symposium on , 19-20 Oct
Page(s): 87 -94, 156

[Abstract]   [PDF Full-Text (404 KB)] **IEEE CNF**

11 **Design of self-synchronized component FSMs for self-timed systems**
Loc Bao Nguen; Perkowski, M.; Jozwiak, L.;
Euromicro Conference, 1998. Proceedings. 24th , Volume: 1 , 25-27 Aug 1998
Page(s): 253 -260 vol.1

[Abstract]   [PDF Full-Text (548 KB)] **IEEE CNF**

12 **Design and simulation of a RISC-based 32-bit embedded on-board computer**
Zhen Guo; He Li; Shuling Guo; Dongsheng Wang;
Test Symposium, 1998. ATS '98. Proceedings. Seventh Asian , 2-4 Dec 1998
Page(s): 413 -416

[Abstract]   [PDF Full-Text (120 KB)] **IEEE CNF**

13 **Using a systems description language for complete avionics systems**
Barton, D.L.; Fortier, S.C.;
Aerospace Conference, 1998. Proceedings., IEEE , Volume: 1 , 21-28 Mar 1998
Page(s): 249 -254 vol.1

[Abstract]   [PDF Full-Text (560 KB)] **IEEE CNF**

14 **VHDL-based EDA tool implementation with Java**
Miller, R.;
VLSI, 1998. Proceedings of the 8th Great Lakes Symposium on , 19-21 Feb 199
Page(s): 440 -445

[Abstract]   [PDF Full-Text (36 KB)] **IEEE CNF**

15 **Electronic design automation (EDA) techniques for the design of pow electronic control systems**
Patterson, E.B.; Holmes, P.G.; Morley, D.;
Circuits, Devices and Systems, IEE Proceedings G , Volume: 139 Issue: 2 , Apr
Page(s): 191 -198

16 **Undergraduate field applications engineers: a successful experiment electronic design automation technology transfer**
Lucic, R.A.; Rohrer, R.A.;
Education, IEEE Transactions on , Volume: 38 Issue: 3 , Aug 1995
Page(s): 261 -265

17 **Virtual prototyping tools for electronic design automation**
Petriu, E.M.; Cordea, M.; Petriu, D.C.;
IEEE Instrumentation & Measurement Magazine , Volume: 2 Issue: 2 , Jun 1999
Page(s): 28 -31

Home | Log-out | Journals | Conference Proceedings | Standards | Search by Author | Basic Search | Advanced Search
Join IEEE | Web Account | New this week | OPAC Linking Information | Your Feedback | Technical Support | Email Alerting
No Robots Please | Release Notes | IEEE Online Publications | Help | FAQ | Terms | Back to Top

IEEE HOME | SEARCH IEEE | SHOP | WEB ACCOUNT | CONTACT IEEE

Membership   Publications/Services   Standards   Conferences   Careers/Jobs

IEEE Xplore®
RELEASE 1.4

Welcome
United States Patent and Trademark O

Help   FAQ   Terms   IEEE Peer Review   Quick Links   » Sea

Welcome to IEEE Xplore

- O– Home
- O– What Can I Access?
- O– Log-out

Tables of Contents

- O– Journals & Magazines
- O– Conference Proceedings
- O– Standards

Search

- O– By Author
- O– Basic
- O– Advanced

Member Services

- O– Join IEEE
- O– Establish IEEE Web Account
- O– Access the IEEE Member Digital Library

🖶 Print Format

Your search matched **2** of **930311** documents.

A maximum of **2** results are displayed, **25** to a page, sorted by **Relevance** in **descending** order.
You may refine your search by editing the current search expression or entering a new one the text b
Then click **Search Again**.

| ((eda and emulation) and ((1950 <in> py) or (1951 <in> py) or (1952 <in> py) or (1953 |

> Search Again

Results:
Journal or Magazine = **JNL**   Conference = **CNF**   Standard = **STD**

1 **Dynamic test emulation for EDA-based mixed-signal test developmen automation**
Jean Qincui Xia; Austin, T.; Khouzam, N.;
Test Conference, 1995. Proceedings., International , 21-25 Oct 1995
Page(s): 761 -770

[Abstract]   [PDF Full-Text (840 KB)] **IEEE CNF**

2 **Undergraduate field applications engineers: a successful experiment electronic design automation technology transfer**
Lucic, R.A.; Rohrer, R.A.;
Education, IEEE Transactions on , Volume: 38 Issue: 3 , Aug 1995
Page(s): 261 -265

[Abstract]   [PDF Full-Text (504 KB)] **IEEE JNL**

Home | Log-out | Journals | Conference Proceedings | Standards | Search by Author | Basic Search | Advanced Search
Join IEEE | Web Account | New this week | OPAC Linking Information | Your Feedback | Technical Support | Email Alerting
No Robots Please | Release Notes | IEEE Online Publications | Help | FAQ | Terms | Back to Top

noury and bonhomme and jullien

Google Search

The **"AND"** operator is unnecessary -- we include all search terms by default.
[details]

Web   Images   Groups   Directory   News

Searched the web for **noury** and **bonhomme** and **jullien**.   Results 1 - 10 of about 18. Search took **0.17** seconds.

Tables des Rubriques de notre Bulletin - [Translate this page]
... **NOURY**-VIGNAIS, Q94-208, **NOURY** Roger (954), 30, 91. ...
PIOT-**BONHOMME**, Q99-185, CHEVREL
Monique (1138), 49, 53. ... PITEUX François de **Jullien** et Julienne BOUJARD, À travers
les ...
membres.geneaguide.com/cgiv/cg35a-38.htm - 86k - Cached - Similar pages

[PDF]Histoire
File Format: PDF/Adobe Acrobat - View as HTML
... 1692 Marguerite POISSON 9 Jean LEBRETON x Laval 1716 Anne **BONHOMME** 8 Guillaume ... Lebreton
(ns) et de Jean Tomin oncle x Avénières 12.7.1678 **Jullien** THOMIN fils ...
perso.wanadoo.fr/odile.halbert/Famille/Lebreton.pdf - Similar pages

[PDF]Clisson - Loire-Atlantique ANNEES 1750 - 1789
File Format: PDF/Adobe Acrobat - View as HTML
... Jean x Renée GIRAUD ° Mouzillon 19-04-1784 NotreDame BLINAUD Pierre p= **Jullien**
x Marie ... Louise p= Jean x Marguerite NEAU 06-02-1776 StJacques **BONHOMME** Pierre p ...
perso.wanadoo.fr/odile.halbert/Table/Clisson2.pdf - Similar pages
[More results from perso.wanadoo.fr]

Ahnenforscher Gm - Go (1) - [Translate this page]
... Bonenfant, Bonfice, Bonfils, Bonheur, **Bonhomme**, Bonhommet, Bonin ... Ne), Julle, Jullemier,
Jullien, Julmier, Jumel ... Notaire, Notta, Nour, **Noury**, Nouvillon, Novisse ...
www.ahnenlotse.de/fafo_gm_g1.htm - 67k - Cached - Similar pages

[PDF]Bibliotheque d'un
File Format: PDF/Adobe Acrobat
... Le **Bonhomme Noury** et son chien Perdreau aux Marais- Verniers vers l'an de grace 1859
[...] ... Le Lièvre. Poème avec une notice et des notes par Ernest **Jullien**. ...
www.argusdubibliophile.com/cat-vente-chasse.pdf - Similar pages

USAJ - Infos - Presse - [Translate this page]
... TGI Angers. GUERRERO Christine épse **NOURY**. TI Laval. ... TGI Bordeaux. PERROT
Sylvie épse **JULLIEN**. TI Bordeaux. ... TI Lyon. **BONHOMME** Maryline épse BROCA. ...
perso.club-internet.fr/fedfaj/ usaj/usaj_capc_011024_TA.htm - 101k - Cached - Similar pages

Simultanés du Roy René - [Translate this page]
... 0424, 46,45, **NOURY** - BARTHE, Bridge Club d'Agen. ... 0441, 46,10, **JULLIEN** - NODET,
ABCDE Laxou Nancy. ... 0515, 43,01, **BONHOMME** - BILLION, Bridge Club de Beynost. ...
www.bridgeplus.com/simultane/200308/classNS.htm - 101k - Cached - Similar pages

Simultanés du Roy René - [Translate this page]
... 0052, 61,60, 243, **JULLIEN** - VUARIER, Bridge-Club de Montélimar. ... 0194, 57,58, 132,
BONHOMME - CLOT, Bridge Club St Exupéry à ... 0542, 51,23, 65, **NOURY** - MARROT, Bridge

Club d ...
www.bridgeplus.com/simultane/200314/classNS.htm - 101k - Cached - Similar pages

Liste des patronymes - [Translate this page]
... 1), BOLLORÉ (1), BONA de PEREX (2), BONAERT (2), BONDON (1), **BONHOMME** (1), BONIAN ... 1),
de NIVET (2), de NOBLET d'ANGLURE (1), de NORMANDE (2), de **NOURY** (1), de ...
membres.lycos.fr/miollisgenea/surname.htm - 101k - Cached - Similar pages

Fédération Française d'Athlétisme - cross inter nord ouest ... - [Translate this page]
... 16:43 **JULLIEN** Audrey CAF (As Querqueville) 470634 75. ... F **BONHOMME** Dominique VEF
(Ascjc St Malo) 406603 84. ... **NOURY** Antoine CAM (Ea Rennes) 572391 114. ...
www.athle.org/html/pages/html/ cross_nordouest_2001.html - 101k - Cached - Similar pages

Google ▶

Result Page: 1 2   **Next**

| noury and bonhomme and jullien | | Google Search | Search within results |

Dissatisfied with your search results? Help us improve.

Google Home - Advertise with Us - Business Solutions - Services & Tools - Jobs, Press, & Help

Google™

functional replicator verification

Google Search

Web | Images | Groups | Directory | News

Searched the web for **functional replicator verification**. Results **1 - 10** of about **482**. Search took **0.19** seconds.

First EDA | Products | ProDesign | CHIPit | IP Development & Use
... have a platform for development and **verification** of their ... the IP with their corresponding
functional and interface ... is used as a common **replicator** platform in ...
www.firsteda.com/products/prodesign/ipdevelopment.htm - 14k - Cached - Similar pages

Services - DVD-Video
... No. In order to burn a **functional** copy of a SafeDisc protected CD the ... The **replicator**
will need to license DCA's SafeDisc Data **Verification** Software to QA ...
www.disctronics.co.uk/disctron/services/ safedisc/safedisc_faq.htm - 34k - Cached - Similar pages

[PDF]CD-ROM Copy Protection
File Format: PDF/Adobe Acrobat - View as HTML
... Can an end-user burn a **functional** copy of S ... S AFE D ISC -enable a **replicator** or mastering ... D
ISC authorization commands initiates the **verification** sequence that ...
www.macrovision.com/solutions/software/ cdrom/SafeDisc_V2_FAQ_April_2002.pdf - Similar pages

[PDF]Generators and the **Replicator** Control Structure in the Parallel ...
File Format: PDF/Adobe Acrobat
... AL- LOY introduces the **replicator** control structure to provide a new view of ... pro-
gramming language based on the programming paradigms of **functional** and object ...
delivery.acm.org/10.1145/100000/93565/ p189-mitsolides.pdf?key1=93565&key2=7205764401&coll=portal&... - Similar pages

[PPT]computing.fnal.gov/cms/bauerdic/baserev_nov_2000/talks/Nov2000_break2_Fisk1.ppt
File Format: Microsoft Powerpoint 97 - View as HTML
... Both require high speed transfers, secure data access, transfer **verification**, integration
of the data ... Database **Replicator Functional** Prototype Architecture. ...
Similar pages

White Papers
... under the category of Storage Pool **Functional** Software. ... SANWorks Virtual **Replicator**
is a storage product that ... 50% by moving save set **verification**, copying, and ...
www.digitalindiasw.com/services/whitepapers/ digital-India-and-storage-management.htm - 41k - Cached - Similar pages

Smartstorage Jukeman key features
... written after this careful **verification** and only then ... Jukeman's "Data **Replicator**"

feature automates the ... Jukeman toolkits are highly **functional**, easy to ...
www.wpoint.co.uk/smartstorage_jukeman_key_feature.htm - 14k - Cached - Similar pages

[PDF]**Functional** Genomics of Plant-Microbe Interactions 10th New ...
File Format: PDF/Adobe Acrobat - View as HTML
... As a basis for **functional** genomic analysis of symbiotic and pathogenic interactions ... the
unigene set which will be publicly available after **verification** of the ...
mycor.nancy.inra.fr/10NPS/pdfs/10NPS_Abstracts.pdf - Similar pages

Desktop Engineering Articles
... 1000EV, a universal port **replicator** that works with ... Software, Introducing Dimension(tm)
- **functional** 3D Printing ... and solid (3D rendered) toolpath **verification**. ...
www.deskeng.com/articles/02/ april/products/main_dig.htm - 96k - Cached - Similar pages

DynamicTL1®: Java TL1 Software Development Kit (SDK)
... core API provides a developer-friendly and highly **functional** interface for ... Provides
metadata generation and message syntax **verification**; Programmatic access to ...
www.monfox.com/dtl1_sdk.html - 32k - Cached - Similar pages

Goooooooooogle ▶
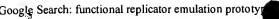Result Page: **1** 2 3 4 5 6 7 8 9 10   **Next**

| functional replicator verification | | Google Search | Search within results |

Dissatisfied with your search results? Help us improve.

Google Home - Advertise with Us - Business Solutions - Services & Tools - Jobs, Press, & Help

Google™

functional replicator emulation prototype

Google Search

Web   Images   Groups   Directory   News

Searched the web for **functional replicator emulation prototype**. Results 1 - 10 of about 29. Search took 0.12 s

[PDF]An Environment for Controlled Worm Replication and Analysis
File Format: PDF/Adobe Acrobat - View as HTML
... 6.1 Basic requirements A **functional** worm replication system ... A **emulation** package which
supported flexible emulated ... Physical Figure 1: A worm **replicator** with one ...
www.research.ibm.com/antivirus/SciPapers/VB2000INW.pdf - Similar pages

LWN: Software announcements by license
... PHP SQL **Replicator**, Default, 0.4 Beta, **Free** for non ... Public License (GPL), A trash
can **emulation** script. ... GPL), A lightweight, but fully **functional** curses frontend to ...
lwn.net/Articles/17871/ - 101k - Cached - Similar pages

[PDF]A Model for Network Worms
File Format: PDF/Adobe Acrobat - View as HTML
... 43 3.5.4 Sample VBA based **Replicator** ... program.....14 Figure 3-2: The
functional organs of ... If the program under **emulation** makes anomalous accesses ...
madchat.org/vx/papers/av/masterthesis.pdf - Similar pages

lists.fresco.org/pipermail/fresco-devel/1998-February.txt
... programming (just like not everyone likes **functional** programming) and ... (I've only
heard about **replicator** too - not ... we will need to provide X-window "**emulation**". ...
101k - Cached - Similar pages

[PDF]This Manual Supports: ThinkPad 340x (2610) ThinkPad 355x (2619) ...
File Format: PDF/Adobe Acrobat - View as HTML
... . . . 472 Printer Checkout . . . . . 474 Port **Replicator** Checkout .
. . . . 475 TrackPoint II Checkout . . . .
www.doe.carleton.ca/~wwf/tpvol2.pdf - Similar pages

[PDF]Schumpeterian competitive dynamics and economic learning: An ...
File Format: PDF/Adobe Acrobat - View as HTML
... short- term consortia, private financing vs. public financial support, **prototype**
development vs. component standardization, and other such choices. ...
www.druid.dk/conferences/summer2002/ Papers/MATHEWS2.pdf - Similar pages

Glossary - R
... Phase 2: **Functional** requirements gathering and specification. ... Entry - Binary Synchronous
Communication) **Emulation** in the ... a separate tray or a port **replicator**. ...
www4.gartner.com/6_help/glossary/GlossaryR.jsp - 68k - Cached - Similar pages

[PS]www.rni.org/kanerva/rwi-sics.ps.gz

File Format: Adobe PostScript - <u>View as Text</u>
... of fMRI Data 49 5.4 Extended **Functional** Connectivity Analysis ... 16.2 Boltzmann Selection
and the **Replicator** Equation 178 ... term for this is "**emulation**." Just about ...
<u>Similar pages</u>

[PDF]<u>Behavioral Economics: Past, Present, Future</u>
File Format: PDF/Adobe Acrobat - <u>View as HTML</u>
... Simon's term) because they posit **functional** heuristics for ... find appealing and worthy
of **emulation** (see Hertwig & ... truly are (unless the **prototype's** extremity is ...
www.hss.caltech.edu/~camerer/ribe239.pdf - <u>Similar pages</u>

<u>www.pwr.wroc.pl/AMIGA/AR/ar322.guide</u>
... **Replicator** - A program to quickly and easily ... or on its own customscreen * VIC **emulation**
is very ... for interesting, entertaining, and **functional** screen saver ...
101k - <u>Cached</u> - <u>Similar pages</u>

Gooogle ▶

Result Page: **1** <u>2</u> <u>3</u>   **Next**

| functional replicator emulation prototype | Google Search | <u>Search within results</u> |

Dissatisfied with your search results? <u>Help us improve.</u>

<u>Google Home</u> - <u>Advertise with Us</u> - <u>Business Solutions</u> - <u>Services & Tools</u> - <u>Jobs, Press, & Help</u>

©2003 Google

Google™

functional replicator verification eda

Google Search

Web | Images | Groups | Directory | News

Searched the web for **functional replicator verification eda**. Results **1 - 10** of about **12**. Search took **0.20** secon

First EDA | Products | ProDesign | CHIPit | IP Development & Use
... CHIPit is used as a common **replicator** platform in ... These hardware extensions can contain
functional modules like hard ... offer a wide range of **verification** methods ...
www.firsteda.com/products/prodesign/ipdevelopment.htm - 14k - Cached - Similar pages

[PS]www.ipd.uka.de/~breitner/assets/970320_Oekonometrie.ps.gz
File Format: Adobe PostScript - View as Text
... different access-profile and **functional** requirements of ... defined Ad-hoc Analytical(**Verification**)
Discovering. ... Migration : CA- Ingres **Replicator**, Carleton Passport ...
Similar pages

[PDF]AUSPEX SYSTEMS, INC.
File Format: PDF/Adobe Acrobat - View as HTML
... development, electronic design analysis ("**EDA**"), seismic and ... This architecture utilizes
Functional Multi-Processing ... Turbo Copy and Turbo **Replicator**, provide for ...
www.auspex.com/news/library/Annual-Report_Fiscal-Q/ FY01form10K_Current.pdf - Similar pages

[PDF] Page 1
File Format: PDF/Adobe Acrobat - View as HTML
Page 1. Page 2. Disclaimer THIS DOCUMENTATION IS PROVIDED FOR REFERENCE
PURPOSES ONLY. WHILE EFFORTS WERE MADE TO VERIFY THE COMPLETENESS ...
doc.notes.net/uafiles.nsf/docs/rn504/$File/readme.pdf - Similar pages

[PS]www.math.utah.edu/pub/tex/bib/futgencompsys.ps.gz
File Format: Adobe PostScript - View as Text
... replicated [Bal91]. **replicator** [WKT00]. ... **verification** [Hol93]. ... An adaptive granularity
control algorithm for theparallel execution of **functional** programs. ...
Similar pages

[PS]www.math.utah.edu/pub/tex/bib/sigplan1990.ps.gz
File Format: Adobe PostScript - View as Text
... **Functional**-Imperative [351]. Functions [1606, 40, 39, 1222, 1613, 41,1426, 1577,
1702, 1661, 138, 730, 727, 1886,. ... **replicator** [64]. Reply [257, 1819]. ...
Similar pages
[More results from www.math.utah.edu]

[PDF]TRoU 0002 Layout.bw
File Format: PDF/Adobe Acrobat - View as HTML

... name and password are encrypted and sent to Apple's server for **verification**. ... a presentation program (a la PowerPoint) and a more **functional** database program. ...
www.trou.org/newsletter/pdfs/nlpdfs/nl_0002.pdf - Similar pages

[PDF]NO. 364 January 2000 Japanese Companies in the United States ...
File Format: PDF/Adobe Acrobat - View as HTML
... An extra $650 will buy the DVD (digi- tal video disc) Network Dock II port **replicator**, which, according to TAIS, turns the Portege 7140CT into a true desktop ...
www.jei.org/Archive/BR00/364x/364.pdf - Similar pages

[PS]www.brics.dk/NS/95/2/TACAS/TACAS.ps.gz
File Format: Adobe PostScript - View as Text
... is to combine the two major paradigms for the **verification** of distributed ... different message values are needed to verify the protocol's **functional** correctness. ...
Similar pages

karkza.mine.nu/ftp/textfiles/wordlists/foldoc.txt
... card punt puppy pure **functional** pure lambda ... repeater replacement replication **replicator** reply requirements ... card verbage verbiage **verification** version vertical ...
79k - Cached - Similar pages

In order to show you the most relevant results, we have omitted some entries very similar to the 10 already displayed.
If you like, you can repeat the search with the omitted results included.

| functional replicator verification eda | Google Search | Search within results |

Dissatisfied with your search results? Help us improve.

Google Home - Advertise with Us - Business Solutions - Services & Tools - Jobs, Press, & Help

Google™

eda fpga

Google Search

Web | Images | Groups | Directory | News

Searched the web for **eda fpga**.   Results **1** - **10** of about **30,900**. Search took **0.18** seconds

---

Eda - Sign up for a FREE DEMO   Sponsored Link
www.mentor.com/dsm   Click Here for IC DESIGN & Verification Tools & SOLUTIONS

Category: Computers > CAD > ... > ASIC PLD FPGA Digital Logic Design

- OPEN-**EDA FPGA** ??
... OPEN-**EDA FPGA** ?? ????: 2001/10/16,Tue @15:23:57 (???: 1182
? - hits: 0). ... OPEN-**EDA FPGA** ??" | ??/???? | 0 ???s. ...
www.icdiy.org/article.php3?sid=36 - 8k - Cached - Similar pages

- OPEN-**EDA FPGA** ????
... OPEN-**EDA FPGA** ???? ????: 2002/01/09,Wed @15:20:34
(???: 1365 ? - hits: 525). ?? ????? OPEN ...
www.icdiy.org/article.php3?sid=40 - 8k - Cached - Similar pages
[More results from www.icdiy.org]

PeakFPGA - **FPGA** Design from Altium
... to enter the **FPGA** Design and Synthesis market. About Altium. Altium is a
leading
global developer and supplier of desktop electronic design automation (**EDA**) and ...
Description: VHDL design, simulation and synthesis tools.
Category: Science > Technology > ... > Design > Verilog and VHDL Tools
www.acc-eda.com/ - 11k - Cached - Similar pages

??**EDA**=»ASIC **FPGA** MCU **EDA** ???
*The summary for this Chinese (Simplified) page contains characters that cannot be correctly displayed in this
language/character set.*
vip.6to23.com/zhangzhenyan/ - 3k - Cached - Similar pages

Electronic Design Automation products and information for board- ...
... Link to **EDA** SeriesDesign entry encapsulates a circuit description ... plans, partitions,
places and routes a cell-based ASIC, maps a netlist into an **FPGA**, or places ...
www.chipcenter.com/edatools/main.html - 36k - Cached - Similar pages

????????
... 1. 4, ????? OPEN-**EDA FPGA** ????, tom, 2002-06-17
03:06. 13. ... 0. 12, OPEN-**EDA FPGA** ????, tom, 2001-11-28 08:11.
10. ...
www.iazone.net/search.php3?topic=FPGA - 13k - Cached - Similar pages

????????
... tom, 2002-06-17 03:06. 5. 8, ????? OPEN-**EDA FPGA** ????, tom,
2002-06-17 03:06. 13. ... 29, OPEN-**EDA FPGA** ????, tom, 2001-11-28 08:11.
10. ...
www.iazone.net/search.php3 - 13k - Cached - Similar pages
[More results from www.iazone.net]

Verilog HDL Simulators - EDA Tools for FPGA and ASIC Digital ...

... Verilog HDL **EDA** Tools for **FPGA** & ASIC Digital Design. ... Simucad provides affordable
Verilog simulation **EDA** tools for **FPGA** and ASIC designs written in Verilog HDL. ...
Description: By Simucad. 64-bit Verilog HDL simulation products for **FPGA** and ASIC design and test. Included
are...
Category: Computers > Programming > Languages > Verilog
www.simucad.com/ - 22k - Cached - Similar pages

Verilog Simulation EDA Tools for FPGA and ASIC Design Engineers

Verilog Simulation **EDA** Tools for **FPGA** and ASIC Design Engineers - Simucad - Provider
of Verilog HDL Logic and Fault Simulators for **FPGA** and ASIC Design ...
www.simucad.com/about/index.shtml - 21k - Cached - Similar pages

channel-e: ??????? ? ?????????

*The summary for this Russian page contains characters that cannot be correctly displayed in this
language/character set.*
www.channel-e.ru/news/pld_eda/pld_archiv.htm - 33k - Mar 28, 2003 - Cached - Similar pages

Goooooooooogle ▶

Result Page: **1** 2 3 4 5 6 7 8 9 10   **Next**

| eda fpga | | Google Search | Search within results |

Dissatisfied with your search results? Help us improve.

Google Home - Advertise with Us - Business Solutions - Services & Tools - Jobs, Press, & Help

©2003 Google

Google™

eda fpga emulation prototype 1999

Google Search

Web | Images | Groups | Directory | News

Searched the web for **eda fpga emulation prototype** 1999. Results **1 - 10** of about **315**. Search took **0.12** secon

Sponsored Links

[PDF]**EDA Tools for SoC**
File Format: PDF/Adobe Acrobat - View as HTML
... Breadboard Breadboard **Emulation Emulation** Instruction-set Simulation
Instruction-set ... **EDA**
Tools for SoC 31 Copyright ©**1999** ... n Designing Complex **FPGA's** - Tips and ...
www.iee-cambridge.org.uk/arc/seminar01/ slides/ColinWalls.pdf - Similar pages

ASIC, EDA, and IC fabrication technologies and information for ...
... creation of a functional hardware **prototype** at the ... simulators, simulation accelerators,
in-circuit **emulation**, and rapid ... substitution of a custom **FPGA** for the ...
www.chipcenter.com/asic/products_100-199/prod107.html - 39k - Cached - Similar pages

FPGA, PLD and programmable logic information for board-level ...
... The number of verification/debugging **EDA** tools, equipment, and ... The line between traditional
emulation and the block ... on the amount of logic placed in each **FPGA**. ...

www.chipcenter.com/pld/pldf054.htm - 39k - Cached - Similar pages
[More results from www.chipcenter.com]

A Tribute to Resource Of Civilization
... Aptix) Synopsys, Verilog, VHDL, OrCAD, **FPGA** (Xilinx, Altera ... Automation Analog, DSP,
Algorithm, Aptix **Emulation** Fast **Prototype** ... unique in today¡s **EDA** market Sub
...
www.geocities.com/tangsystem/RESUME.html - 20k - Cached - Similar pages

Technical Papers
... Browse eCatalog: Subscribe to **EDA** Daily News. eCatalog Asic & IC PCB
FPGA Design
Services Hardware SIP, ... Closure Using Solidify and Hardware **Emulation**; Deep
sub ...
www.edatoolscafe.com/TECHNICAL/techpapers.html - 33k - Cached - Similar pages

[PDF]**No Slide Title**
File Format: PDF/Adobe Acrobat - View as HTML
... SoC?**EDA**???? ... Time-to-**Emulation**
- ????? ... IP IP **FPGA FPGA Prototype Prototype** SoC???? ...
www.mentorg.co.jp/event/eds2002/eds0130/pdf/soc.pdf - Similar pages

(ESNUG 343 Item 6) --- ...
... for those who are using FPGAs to **prototype** their ASIC ... to know about **FPGA** synthesis
for **emulation**: "If we ... count exceeds the capacity of your current **FPGA** w/ DC ...
www.deepchip.com/items/0343-06.html - 16k - Cached - Similar pages

(SNUG 00 Item 15) --- ...
... LMC Hardware Models ### 6% Aptix **Emulation** # 2% Denali ... w/ FPGAs ##### 31%
we don't **prototype** w/ FPGAs ... **FPGA** Compiler (fpga_shell) and **FPGA** Compiler II ...
www.deepchip.com/items/snug00-15.html - 11k - Cached - Similar pages
[More results from www.deepchip.com]

Welcome to APTIX Corporation - Realtime Reconfigurable ...
... Based Verification Tools Cited by **EDA** Consortium Alongside ... Oct 1997 APTIX SUPPORTS
XILINX XC4000XV **FPGA** FAMILY. ... PROFITABLE QUARTERS FOR SYSTEM **EMULATION** COMPANY. ...
www.aptix.com/news/press_archives.htm - 47k - Cached - Similar pages

[PDF]Speed Limits in Rapid Prototyping
File Format: PDF/Adobe Acrobat - View as HTML
... **Emulation** and Prototyping are complementary techniques: **emulation** is mandatory to
consolidate large RTL designs, An **FPGA**-based **prototype** is becoming ...
www.eda-expos.co.uk/dve/Docs/Pavesi.pdf - Similar pages

Goooooooooogle ▶
Result Page: **1** 2 3 4 5 6 7 8 9 10   **Next**

| eda fpga emulation prototype 1999 | Google Search | Search within results |

Dissatisfied with your search results? Help us improve.

Google Home - Advertise with Us - Business Solutions - Services & Tools - Jobs, Press, & Help

©2003 Google